Formal Worst-Case Timing Analysis of Ethernet Topologies with Strict-Priority and AVB Switching

Jonas Diemer, Daniel Thiele, Rolf Ernst Institute of Computer and Network Engineering Technische Universität Braunschweig 38106 Braunschweig, Germany {diemer|thiele|ernst}@ida.ing.tu-bs.de

Erratum as of 2012-07-27

- 1) Pg. 8: The The latencies for communication between the nodes and the controller amount to 1002~ns, not $1002~\mu s$. This results from the sum of 672~ns for the transfer time and 330~ns for the wire delay. Thanks to Steffen Stein for pointing out this typo.
- 2) All experiments in the paper are performed for 1GBit Ethernet, which is not explicitly mentioned.